1. 1, 0, 1 OR 0, 1, 1
2. The NOT changes the lower gate to 0. When it then moves to the middle gate (AND) the incoming values are 1 and 0, hence 0 is passed through. As such the top OR gate is receiving 0 and 0 and passes through the 0.
3. In the beginning when both are set to 0, the top gate passes the 0 to the NOT, passing a 1 down to the bottom gate which returns a 1 through the OR gate, gets to the NOT passes a 0 as final output. Changing the top gate to 1 means that a 0 is passed after the NOT gate, goes to the bottom and makes the OR gate pass a 0, the final output will be a 1 after passing through the final NOT gate.
4. *Need to read up / find an answer on this, maybe discuss with Adam.*
5. *Need to read more.*
6. a. 6AF2  
   b. E85517  
   c. 48  
   *^ Need to go back and revise hexadecimal. I can do the conversion fine but knowing what the numbers mean without a table I need to practice.*